



U.S. DEPARTMENT OF
ENERGY

Welcome

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Under Secretary for Science
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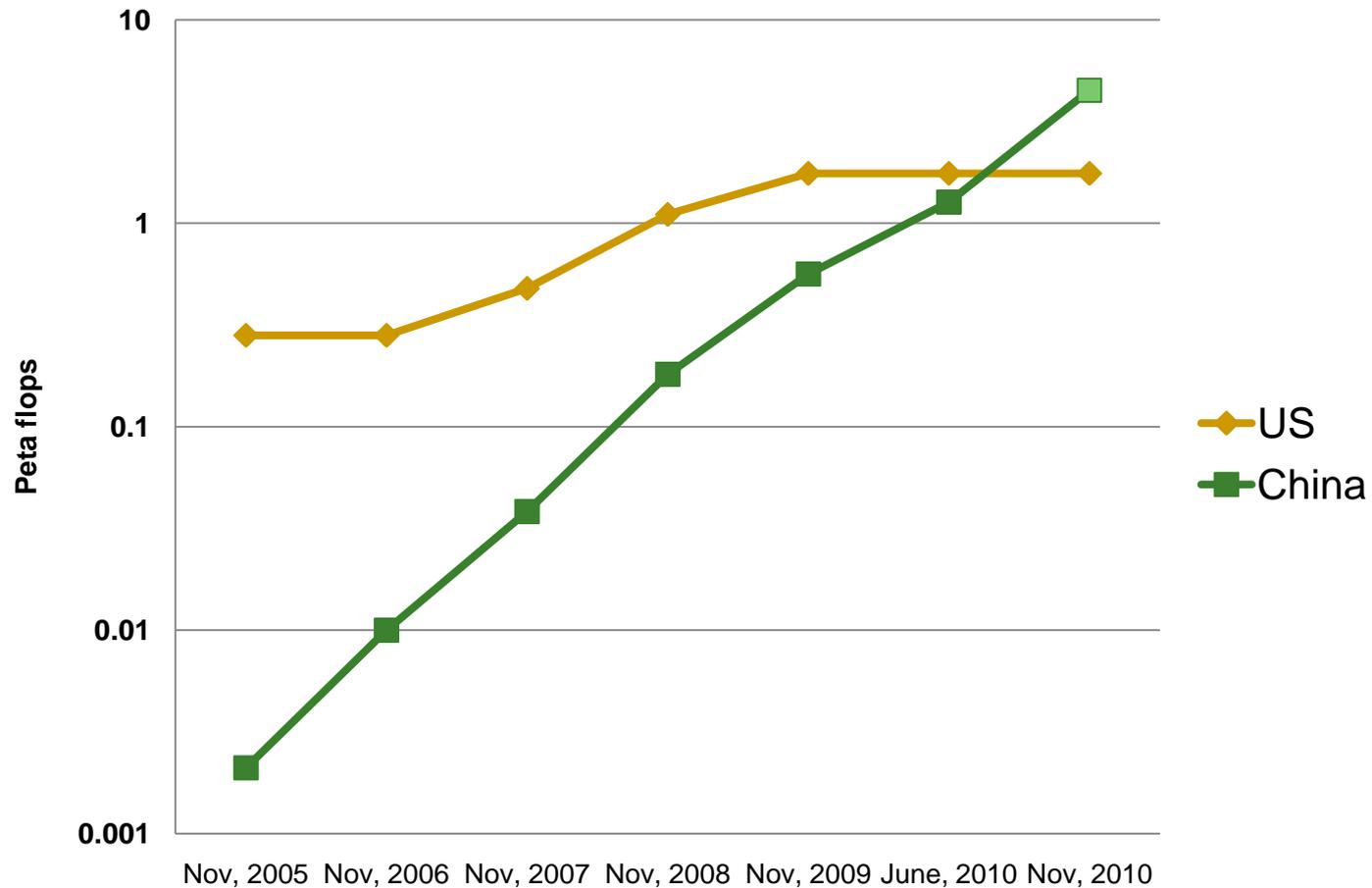
A computer from my past

CDC 7600s at Livermore



- 27.5 ns clock cycle
- 65 Kword primary memory using core and variable-size (up to 512 Kword) secondary memory
- About 10 MFLOPS on hand-compiled code, with a peak of 36 MFLOPS

We're not the only ones



Goals for today

- Promote awareness of the full simulation picture
 - Software, hardware, methodologies
- Describe current capabilities and successes
- What are the barriers and how can we address them?
 - Deployment of existing capabilities from labs and academia
 - Improvement of existing capabilities

Today's agenda

- **Welcome:** Koonin
- **Keynote:** Moniz
- **Introduction:** Chu
- **Current Status of HPC in the U.S.:** Simon
- **Panel 1:** Industry
- **Materials:** Glotzer (working lunch)
- **Panel 2:** Academia
- **Panel 3:** Cooperation
- **Summary:** Donofrio